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A Hybrid PPSOGSA Algorithm for Optimal Volt/VAr/THDv Control in Distorted Radial Distribution Systems

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ABSTRACT

This paper presents a new hybrid PPSOGSA algorithm based on the phasor particle swarm optimization (PPSO) and gravitational search algorithm (GSA) for solving the Volt/VAr/THDv control optimization problem in radial distribution systems with nonlinear loads. The PPSOGSA's performance is analyzed and evaluated using the standard IEEE 30-bus radial distribution test system. The optimal solutions are obtained based on the following three optimization criteria: minimization of total active power losses, minimization of the voltage deviation, and a simultaneous minimization of total active power losses and voltage deviation. The voltage total harmonic distortion levels (THDv) are estimated using the decoupled harmonic power flow (DHPF) algorithm. Simulation results, obtained using the proposed PPSOGSA-based approach, are compared with those obtained using the other optimization algorithms such as PSO, PPSO, GSA, and PSOGSA. It is shown that the PPSOGSA-based approach has better performance in comparison to the other optimization techniques.

KEYWORDS

optimal reactive power flow; harmonic distortion; voltage control; hybrid algorithm; phasor particle swarm optimization; gravitational search algorithm

Introduction

The voltage and reactive power control or Volt/VAr control have been widely employed to reduce power losses and satisfy the main distribution system operational constraints such as voltage magnitude limits and line flow limits. Volt/VAr optimization implies the coordination of voltage regulating devices, such as the substation under load tap changer (ULTC) and distribution line voltage regulators (VRs), and VAr control devices, such as switched shunt capacitors (SSCs) and static VAr compensators (SVCs), of distribution systems in real time with aims to achieve one or more specified objectives (Radosavljević 2018). Integration of distributed generation (DG) in a distribution system, which can also be operated in Volt/VAr control mode, may significantly affect system performance. Depending on the available regulation resources, the control variables can be tap changer settings of ULTC and VRs, voltage magnitudes of DGs modeled as PV buses, and reactive powers of SSCs and SVCs. According to the available regulation devices, the control variables can be discrete (tap positions of ULTC and VRs, and number/size of capacitors) or continuous (voltage magnitudes and reactive power outputs of DGs, and reactive powers of SVCs).

In recent years, the widespread use of non-linear loads, such as adjustable speed drives and rectifiers, within the distribution system leads to power quality problems. One of the most important aspects of power quality is the presence of harmonics in the power system. High levels of THD_V could lead to classical problems associated with harmonic distortion, such as equipment overheating, a reduction of efficiency in power generation, transmission and utilization, loss of reliability and communication interference. Although they do not generate harmonics, capacitors may have an important role in the propagation of harmonics in the systems. Addition of shunt capacitors in a harmonics polluted distribution system could lead to resonant conditions and increase the voltage distortion levels. In order to overcome these problems, in Volt/VAr control scheme it is necessary to take into consideration constraints which include voltage harmonic distortion limits in accordance with the IEEE-519 standard (1993). However, taking harmonics into account, Volt/VAr control problem becomes more complicated and difficult to solve.

In its most general formulation, the optimal Volt/VAr control problem can be described as a non-linear large-scale static optimization problem with both continuous and discrete control variables. The optimal solution of this problem aims to optimize a chosen objective function through an optimal adjustment of the power system control variables, while various network operational constraints at different load levels are satisfied. The different optimization techniques, such as the genetic algorithm (GA) (Hu et al. 2003; Jashfar and Esmaeili 2014; Ulinuha, Masoum, and Islam 2011), evolutionary programming (Madureira and Lopes 2009), ant colony optimization (ACO) (Niknam 2008), gravitational search algorithm (GSA) (Wang et al. 2015) and particle swarm optimization (PSO) (Auchariyamet and Sirisumrannukul 2009; Chen et al. 2015) have been applied for finding an optimal solution for the Volt/VAr problem. In Hu et al. (2003), the authors have presented a Volt/VAr control strategy to solve the off-line setting control problem by coordinating switching capacitors and ULTC setting at substations based on the day-ahead load forecast. In Ulinuha, Masoum, and Islam (2011), the authors used a GA to perform load divisions and proposed a hybrid genetic-fuzzy algorithm to simultaneously control voltage profile, reactive power flow, and total harmonic distortion of distorted distribution systems. In another research by Jashfar and Esmaeili (2014), the reactive power of photovoltaic systems is also scheduled and considered as an additional control variable. Madureira and Lopes (2009) have proposed an artificial neural network (ANN) approach for coordinated voltage support in distribution networks with large integration of DGs and microgrids. The daily Volt/VAr control problem in distribution networks considering DGs with the objective of minimization of electrical energy costs

is presented in (Niknam 2008). Wang et al. (2015) suggested application of ULTC, capacitor banks, and DGs to the Volt/VAr control with the objective function of active power losses minimization. In Auchariyamet and Sirisumrannukul (2009), the authors have proposed an optimal dispatch of the ULTC, substation capacitors, and feeder capacitors for Volt/VAr control. An efficient methodology for optimal Volt/VAr control of distribution networks with DGs based on fuzzy adaptive hybrid PSO is proposed in Chen et al. (2015).

This paper proposes a new hybrid PPSOGSA method based on phasor particle swarm optimization (PPSO) (Ghasemi et al. 2018) and gravitational search algorithm (GSA) (Rashedi, Nezamabadi-pour, and Saryazdi 2009) to control voltage profile, reactive power flow, and total harmonic distortion of voltage (i.e. Volt/VAr/THD_V control) in radial distribution systems with nonlinear loads. The proposed method is tested on the distorted IEEE 30-bus test system, so that the optimal solutions are obtained for different objectives that reflect the reduction of active power losses and minimization of the voltage deviation. The THDv levels are estimated using the decoupled harmonic power flow (DHPF) method (Ulinuha and Masoum 2007). The results obtained by the proposed method are compared with PSO, PPSO, GSA, and PSOGSA. Also, in order to validate the accuracy of the DHPF method, simulation results are compared to those generated by the Harmonic Analysis module of the ETAP programme (2014). These comparisons represent an additional contribution to the field of research.

Problem Formulation

In this paper, the goal of the optimal Volt/VAr/THD_V control in a distribution system with non-linear loads is to find an optimal dispatch of ULTC positions, i.e., the values of the voltage on the secondary side of the transformer equipped with the ULTC, the voltage module values of DGs modeled as PV buses, i.e., the reactive power injections from DGs and values of reactive power of capacitors for each load level. Generally, this problem can be described as follows (Radosavljević 2018):

$$\min F(\mathbf{x}, \mathbf{u}) \tag{1}$$

Subject to:

$$g(\mathbf{x}, \mathbf{u}) = 0 \tag{2}$$

$$h(\mathbf{x}, \mathbf{u}) \le 0 \tag{3}$$

$$\mathbf{u} \in \mathbf{U}$$
 (4)

where $F(\mathbf{x},\mathbf{u})$ is a scalar objective function, $g(\mathbf{x},\mathbf{u})$ is a vector composed of equality constraints, $h(\mathbf{x},\mathbf{u})$ is a vector composed of inequality constraints, \mathbf{x} is a vector of dependent variables, \mathbf{u} is a vector of control variables and \mathbf{U} is a feasible space/region.

For a distribution system polluted by harmonics, having one power transformer with ULTC, N_{bus} buses, N_L lines, N_C capacitors and N_{DG} DGs that operate as PV buses, the vector of control variables **u** consists of the tap position of the ULTC (TAP^L), reactive power of capacitors (Q_C^L) and voltage magnitude of DG units (V_{DG}^L) for each load level L. Accordingly, the vector **u** can be expressed as

$$\mathbf{u}^{\mathrm{T}} = [TAP^{L}, Q^{L}_{C,1}, \dots, Q^{L}_{C,N_{C}}, V^{L}_{DG,1}, \dots, V^{L}_{DG,N_{DG}}]$$
(5)

The vector of dependent variables **x** consists of the RMS bus voltages (V_{RMS}^L) , total harmonic voltage distortions (THD_V^L) , DGs reactive power outputs (Q_{DG}^L) , and line loadings (S_l^L) for each load level *L*. Therefore, the vector **x** can be expressed as

$$\mathbf{x}^{\mathrm{T}} = [V_{RMS,1}^{L}, \dots, V_{RMS,N_{bus}}^{L}, THD_{V,1}^{L}, \dots, THD_{V,N_{bus}}^{L}, Q_{DG,1}^{L}, \dots, Q_{DG,N_{DG}}^{L}, S_{l,1}^{L}, \dots, S_{l,N_{L}}^{L}]$$
(6)

Objective Functions

This paper considers three objective functions which are described below.

Minimization of the Total Active Power Losses

$$\min F(\mathbf{x}, \mathbf{u}) = \min P_{loss}^{L}(\mathbf{x}, \mathbf{u}) = \min \left\{ \sum_{h=1}^{h_{\max}} P_{loss,T}^{L}(f_h) + \sum_{h=1}^{h_{\max}} \left(\sum_{i=1}^{N_L} P_{loss,Li}^{L}(f_h) \right) \right\}$$
(7)

where P_{loss}^{L} is total active power losses for load level L, $P_{loss,T}^{L}(f_h)$ is a component of transformer active power losses at frequency f_h for load level L, $P_{loss,Li}^{L}(f_h)$ is a component of active power losses on line *i* at frequency f_h for load level L, N_L is the total number of lines and h_{max} is the maximum harmonic order under consideration.

Minimization of the Voltage Deviation

$$\min F(\mathbf{x}, \mathbf{u}) = \min V_{dev}^{L}(\mathbf{x}, \mathbf{u}) = \min \sum_{i=1}^{N_{bus}} \left| V_{RMS,i}^{L} - V_{rated}^{L} \right|$$
(8)

where V_{dev}^{L} is the voltage deviation for load level L and V_{rated}^{L} is the rated operating voltage.

Simultaneous Minimization of the Total Active Power Losses and Voltage Deviation

$$\min F(\mathbf{x}, \mathbf{u}) = \min \left\{ w_P P_{loss}^L(\mathbf{x}, \mathbf{u}) + w_V V_{dev}^L(\mathbf{x}, \mathbf{u}) \right\}$$
(9)

where w_P and w_V are the weighting factors for the functions of active power losses and voltage deviation, respectively.

Problem Constraints

At the fundamental frequency, the equality constraints (2) relating to the active and reactive powers at bus i for each load level can be expressed as

$$P_{G,i}^{L}(f_{1}) - P_{D,i}^{L}(f_{1}) = V_{i}^{L}(f_{1}) \sum_{j=1}^{N_{bus}} V_{j}^{L}(f_{1}) Y_{i,j}^{L}(f_{1}) \cos\left(\theta_{i,j}^{L}(f_{1}) - \delta_{i}^{L}(f_{1}) + \delta_{j}^{L}(f_{1})\right)$$

$$(10)$$

$$Q_{G,i}^{L}(f_{1}) - Q_{D,i}^{L}(f_{1}) = V_{i}^{L}(f_{1}) \sum_{j=1}^{N_{bus}} V_{j}^{L}(f_{1}) Y_{i,j}^{L}(f_{1}) \sin\left(\theta_{i,j}^{L}(f_{1}) - \delta_{i}^{L}(f_{1}) + \delta_{j}^{L}(f_{1})\right)$$
(11)

where $i=1, \ldots, N_{bus}$ is the bus number, *L* is the load level, $P_{G,i}^{L}(f_1)$ and $Q_{G,i}^{L}(f_1)$ are the fundamental active and reactive power generations at bus *i* for load level *L*, $P_{D,i}^{L}(f_1)$ and $Q_{D,i}^{L}(f_1)$ are the fundamental active and reactive load demands at bus *i* for load level *L*, $V_j^{L}(f_1)$ is the fundamental voltage at bus *j* for load level *L*, $Y_{i,j}^{L}(f_1)$ is the (*i*,*j*)th element of the fundamental admittance matrix corresponding to the *i*th row and the *j*th column for load level *L*, $\theta_{i,j}^{L}(f_1)$ is the angle of the (*i*,*j*)th element of the fundamental admittance matrix for load level *L*, and $\delta_i^{L}(f_1)$ and $\delta_j^{L}(f_1)$ are the fundamental voltage angles at the buses *i* and *j* for load level *L*, respectively. The backward-forward sweep (BFS) method (Cheng and Shirmohammadi 1995) was used to obtain the parameters of the considered distribution system at the fundamental frequency.

The equality constrains related to the harmonic power flow are defined as:

$$\mathbf{V}^{L}(f_{h}) = \left[\mathbf{Y}_{\mathbf{BUS}}^{L}(f_{h})\right]^{-1} \mathbf{I}^{L}(f_{h})$$
(12)

where $\mathbf{V}^{L}(f_{h})$ is the system bus voltage vector at frequency f_{h} for load level *L*, $\mathbf{Y}_{BUS}^{L}(f_{h})$ is the system bus admittance matrix at frequency f_{h} for load level *L*, and $\mathbf{I}^{L}(f_{h})$ is the system bus injected current vector at frequency f_{h} for load level *L*. The DHPF method (Ulinuha and Masoum 2007) was used to estimate harmonic components.

The inequality constraints (3) include the bus voltage quality constraints of the system, DGs reactive power output limits and line flow limits for each load level. The values of bus voltage magnitudes are bounded by their lower (0.95 p. u.) and upper (1.05 p.u.) limits as follows:

$$V_{RMS}^{\min} \le \sqrt{\sum_{h=1}^{h_{\max}} |V_i^L(f_h)|^2} \le V_{RMS}^{\max}, i = 1, \dots, N_{bus}$$
 (13)

where V_{RMS}^{\min} and V_{RMS}^{\max} are the minimum and maximum bus voltage limits, respectively.

According to IEEE Std. 519–1992 (1993), for distribution systems of 69 kV and below, the voltage total harmonic distortion must be lower than 5%, i.e.

$$THD_{V,i}^{L}(\%) = \frac{1}{|V_{i}^{L}(f_{1})|} \sqrt{\sum_{h\neq 1}^{h_{max}} |V_{i}^{L}(f_{h})|^{2} \times 100(\%)} \le THD_{V}^{max}, i = 1, \dots N_{bus}$$
(14)

where THD_V^{max} is the maximum acceptable level of the THD_V at any bus *i* and load level *L*.

The constraints related to DGs reactive powers and power flows through network lines are:

$$Q_{DG,i}^{\min} \le Q_{DG,i}^L \le Q_{DG,i}^{\max}, i = 1, \dots, N_{DG}$$

$$\tag{15}$$

$$S_{l,i}^{L} \le S_{l,i}^{\max}, i = 1, \dots, N_{L}$$
 (16)

where $Q_{DG,i}^{\min}$ and $Q_{DG,i}^{\max}$ are the minimum and the maximum reactive power limits of the DG at bus *i* for load level *L*, while $S_{l,i}^{\max}$ denotes the maximum power flow in branch *i*.

The set of constraints (4) defines the feasible region of the problem control variables. In particular, in this case the control variables are the positions of the ULTC, the terminal voltages at PV buses, and reactive powers of capacitors. The constraints of the control variables are given as follows:

$$TAP^{\min} \le TAP^{L} \le TAP^{\max} \tag{17}$$

$$V_{DG,i}^{\min} \le V_{DG,i}^{L} \le V_{DG,i}^{\max}, i = 1, \dots, N_{DG}$$
 (18)

$$Q_{C,i}^{\min} \le Q_{C,i}^{L} \le Q_{C,i}^{\max}, i = 1, \dots, N_C$$
 (19)

where TAP^{L} is the ULTC tap position at load level *L*; TAP^{\min} and TAP^{\max} are the minimum and the maximum tap positions of the ULTC, respectively; $V_{DG,i}^{\min}$ and $V_{DG,i}^{\max}$ are the minimum and the maximum voltage magnitudes of the DG at bus *i*; $Q_{C,i}^{\min}$ and $Q_{C,i}^{\max}$ are the minimum and the maximum reactive power limits of the capacitor at bus *i*, respectively.

Expanded Objective Function

The inequality constraints described by expression (3) are taken into account through quadratic penalty factors by means of which the objective function F is expanded in the following manner:

$$F_e = F + \sum_{i=1}^p \lambda_i \left(x_i - x_i^{\lim} \right)^2 \tag{20}$$

where F_e is the expanded objective function that will be minimized, λ_i is the corresponding penalty factor, p is the number of inequality constraints, x_i^{lim} is an upper or lower bound on dependent variable x_i , which is defined by $x_i^{\text{lim}} = x_i^{\text{max}}$ if $x_i > x_i^{\text{max}}$, and $x_i^{\text{lim}} = x_i^{\text{min}}$ if $x_i < x_i^{\text{min}}$. The penalty factor of 10^3 is selected for all the inequality constraints.

The Hybrid PPSOGSA Algorithm

The proposed PPSOGSA approach, which is similar to PSOGSA (Mirjalili and Hashim 2010), hybridizes PPSO (Ghasemi et al. 2018) with GSA (Rashedi, Nezamabadi-pour, and Saryazdi 2009) in order to combine their strengths and overcome their shortcomings. The control parameters of PSOGSA c_1 and c_2 , which are important for the ability to explore and converge, are fixed during iteration process and different combination values of these parameters provide good solutions for different problems. Instead of using fixed value of c_1 and c_2 , in this new hybrid PPSOGSA algorithm, the periodic nature of trigonometric sine and cosine functions is utilized to represent the control parameters through phase angle θ . By doing this, the velocity and position of the *i*th particle in each iteration are updated using the following equations:

$$\mathbf{V}_{i}(t+1) = r_{1}\mathbf{V}_{i}(t) + r_{2}|\cos(\theta_{i}(t))|^{2\sin(\theta_{i}(t))}\mathbf{ac}_{i}(t) + r_{3}|\sin(\theta_{i}(t))|^{2\cos(\theta_{i}(t))}(\mathbf{gbest}(t) - \mathbf{X}_{i}(t))$$
(21)

$$\mathbf{X}_i(t+1) = \mathbf{X}_i(t) + \mathbf{V}_i(t+1)$$
(22)

where the phase angle of particle $i(\theta_i)$ is calculated for the next iterations through the following formula:

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$$\theta_i(t+1) = \theta_i(t) + |\cos(\theta_i(t)) + \sin(\theta_i(t))| 2\pi$$
(23)

In (21) – (23), variables have the following meaning: i = 1, ..., N is the agent number; $\mathbf{V}_i(t)$ and $\mathbf{X}_i(t)$ are the velocity and the position of agent *i* at iteration *t*, respectively; r_1, r_2 , and r_3 are random numbers between 0 and 1; $\mathbf{ac}_i(t)$ is the acceleration of agent *i* at iteration *t* and $\mathbf{gbest}(t)$ is the best position of all particles in the group at iteration *t*. The values of \mathbf{ac}_i and \mathbf{gbest} in Equation (21) are obtained as in Mirjalili and Hashim (2010).

PPSOGSA Implementation

The control variables of the Volt/VAr/THD_V control problem constitute the individual position of several agents that represent a complete solution set. In a system with N agents, the position of agent i is defined by

$$\mathbf{X}_{i}(t) = \begin{bmatrix} x_{i}^{1}(t), \dots, x_{i}^{d}(t), \dots, x_{i}^{n}(t) \end{bmatrix} \text{ for } i = 1, 2, \dots, N \text{ and}$$

$$n = N_{C} + N_{DG} + 1$$
(24)

For each load level, the elements of X_i are the tap position of the ULTC, reactive power outputs of capacitors, and generator bus voltages. The different steps of the PPSOGSA-based approach for the considered optimization problem are the following:

- 1. Search space identification. Initialize PPSOGSA parameters: total number of agents (*N*), total number of iterations (t_{max}), initial value of the gravitational constant (G_0), and user-specified constant (α).
- 2. Initialization: Randomly generate an initial population of *N* agents with their own phase angle through uniform distribution $\theta_i(0) = U(0, 2\pi)$, and with initial velocity within the velocity bound. The initial positions of each agent are randomly selected between the minimum and maximum values of the control variables.
- 3. Set the index of iteration t= 1.
- 4. For each particle in the population, run BFS power flow and DHPF to obtain the power losses, bus RMS voltages, and THD_V values.
- 5. Calculate the fitness value for each agent. In this paper, three different objective functions are considered. Their fitness values are calculated according to Equations (7) (9) and (20).
- 6. Calculate accelerations for all agents.
- 7. Update the velocity and position of all agents by Equations (21) and (22), respectively.
- 8. If the stop criteria is satisfied (i.e. the maximum number of iterations is reached), go to step 9; otherwise, set iteration index t = t + 1, and return to step 4.

9. Return the best solution found. Print out the optimal solution (the best position and the corresponding fitness value) to the considered problem. Stop.

Results and Discussion

The proposed hybrid PPSOGSA-based approach to solve the optimal Volt/VAr/THD_V control problem was evaluated using the IEEE 30-bus distorted radial distribution test system. The approach was implemented in MATLAB 2017b computing environment. To verify the simulation results and compare the performance of the proposed approach, the same problem was solved using PSO, PPSO, GSA, and PSOGSA. The setting parameters of these algorithms are adopted as follows: for PSO, c_1 and c_2 are set to 2 and the inertia weight (w) decreases linearly from 0.9 to 0.4 in each iteration; for GSA, α is set to 20 and G_0 is set to 100; for the hybrid PSOGSA, c_1 and c_2 are set to 2, α is set to 20, and G_0 is set to 1; for the hybrid PPSOGSA algorithm the parameters α and G_0 are set up as well as for PSOGSA. The population size (N) and maximum number of iterations (t_{max}) are set to 30 and 100, respectively, for all case studies. All simulation data were obtained using a PC with a CPU at 2.70 GHz and 8.0 GB RAM. In order to demonstrate the effectiveness, robustness, and high-quality of the proposed hybrid PPSOGSA method, three different objectives were considered. For each function under consideration, 10 consecutive test runs have been performed and the results presented here represent the best values obtained over these 10 runs.

System data for the IEEE 30-bus test system can be found in Fuchs and Masoum (2008). The base voltage and base power for this system are 23 kV and 100 MVA, respectively. The system equivalent impedance was determined from three-phase short-circuit power and reactance-to-resistance ratio at the utility connection point. These values are 1000 MVA and 22.2, respectively (Abu-Hashim et al. 1999). It is assumed that the ULTC is installed on the high-voltage side of the substation transformer winding. The ULTC has 17 possible positions and it can change the voltage from -5% to +5%, so each tap position is rated for a 0.625% voltage adjustment. The system includes one PWM adjustable-speed drive (ASD type 2) rated at 750 kW and 500 kVAr, and located at bus 7. Harmonic spectrum of this non-linear load is taken from (Ulinuha, Masoum, and Islam 2011). In addition, the system contains one linear DG unit which is directly connected to the grid and seven capacitors. The characteristics of DG, capacitors as well as the ULTC are given in Table 1.

At the fundamental frequency, 50% of the load of each bus is modeled as constant power load and the other 50% as constant impedance load. All capacitors are modeled as constant impedances. The DG connected at bus 8 has constant active power output of 5 MW, whereas operates in PV mode with reactive power output in the range of -4 to 4 MVAr. At harmonic frequencies,

Devices	ULTC				Capacitors				DG
Control variables	TAP	Q _{C1} (kVAr)	Q _{C2} (kVAr)	Q _{C3} (kVAr)	Q _{C4} (kVAr)	Q _{C5} (kVAr)	Q _{C6} (kVAr)	Q _{C7} (kVAr)	<i>V_{DG}</i> (r.j.)
Location	0-1	1	1	13	15	19	23	25	8
Min.	-8	0	0	0	0	0	0	0	0.95
Max.	8	900	600	600	600	300	900	900	1.05
Step	1	100	100	150	150	150	150	150	Continual

 Table 1. Settings of control variables in the IEEE 30-bus test system.

linear loads are represented by the parallel RL model (Abu-Hashim et al. 1999), and the non-linear load is treated as a decoupled harmonic current source. The linear DG produces no harmonics and can be represented by an inductive reactance. For this study, the equivalent reactance of the DG unit is the sub-transient reactance of 15%.

In this particular case, the aim of the optimal Volt/VAr/THD_V control is to find the optimal dispatching schedule of the ULTC position, the optimal voltage at the PV bus, and optimum values of reactive power of capacitors for each load level in the daily load diagram, where the bus voltages and harmonic distortions are within the acceptable limits. The daily normalized load diagram of Figure 1 is used for all loads. The following cases are studied.

Case 1: The System Initial Condition without Control Scheme

To examine the impact of shunt capacitors and DG on the voltage profile, harmonic distortion and active power losses, three system conditions are analyzed herein. The first one, named Case 1a, represents a system condition in which all capacitors as well as the DG unit are turned off for all load levels. The second condition (Case 1b) refers to a condition in which all capacitors are turned on and generate their maximum reactive power, while DG is turned off for all load levels. The third condition (Case 1c) corresponds to a system condition that is similar to the second one, but DG is turned on and only generates its maximum active power. The tap position of the ULTC is fixed at



Figure 1. Daily load diagram.

				Min.	Max.	Min.	Max.
System	Load		$V_{dev}^{L}(\mathbf{p})$	THD _V	THD _V	V _{RMS}	V _{RMS}
conditions	level	P ^L _{loss} (kW)	u.)	(%)	(%)	(p.u.)	(p.u.)
Case 1a: Capacitors and DG are turned off	0.5	221.2802	1.0081	0.4559	3.7688	0.9304	1.0000
	0.7	436.1816	1.4118	0.6191	5.2542	0.9025	1.0000
	1.0	899.9744	2.0221	0.8414	7.4442	0.8602	1.0000
	0.8	571.5929	1.6145	0.6961	5.9878	0.8885	1.0000
	0.9	726.0636	1.8178	0.7701	6.7172	0.8744	1.0000
	0.6	319.5034	1.2097	0.5391	4.5146	0.9165	1.0000
Case 1b: Capacitors are turned on and DG is	0.5	207.9386	0.7395	0.7119	6.4427	0.9481	1.0001
turned off	0.7	398.6230	1.1455	0.8838	8.3076	0.9204	1.0001
	1.0	826.1663	1.7604	1.0950	10.8689	0.8784	1.0001
	0.8	521.9651	1.3495	0.9590	9.1832	0.9065	1.0001
	0.9	664.3602	1.5545	1.0290	10.0346	0.8925	1.0001
	0.6	294.0313	0.9422	0.8021	7.3982	0.9342	1.0001
Case 1c: Capacitors and DG are turned on	0.5	98.0977	0.2605	0.5334	4.4031	0.9918	1.0232
	0.7	122.3176	0.2087	0.6914	5.9479	0.9824	1.0039
	1.0	281.8849	0.7745	0.8905	8.0559	0.9431	1.0000
	0.8	158.9972	0.3876	0.7620	6.6755	0.9697	1.0000
	0.9	212.1507	0.5808	0.8281	7.3771	0.9564	1.0000
	0.6	102.0343	0.1486	0.6155	5.1913	0.9871	1.0136

 Table 2. Simulation results of the IEEE 30-bus system for all load levels in the case without control scheme.



Figure 2. Simulation results obtained for Case 1a: (a) voltage profiles and (b) THD_V levels.

0 (that means the voltage at bus 1 is 1 p.u.) for all considered system conditions and load levels. Simulation results are shown in Table 2 and Figure 2–4. Figure 2a,b correspond to Case 1a, Figure 3a,b correspond to Case 1b, while Figure 4a,b correspond to Case 1c.

Based on the obtained results in Figure 2a, it can be seen that the most of the bus voltages violate the allowable voltage limit of 0.95 p.u. for all load levels. In addition, from Figure 2b it is evident that the THD_V of some buses for load levels L= 0.7 to L= 1.0 exceeds the permissible value of 5%. In the case when all capacitors as well as the DG unit are turned off (Case 1a), the total active power losses for load levels of 0.5, 0.7, 1.0, 0.8, 0.9, and 0.6 are, respectively, 221.2802 kW, 436.1816 kW, 899.9744 kW, 571.5929 kW, 726.0636 kW, and 319.5034 kW, while the voltage deviations are, respectively, 1.0081 p.u., 1.4118 p.u., 2.0221 p.u., 1.6145 p.u., 1.8178 p.u., and 1.2097 p.u. The total system energy losses are 10,775.782 kWh.



Figure 3. Simulation results obtained for Case 1b: (a) voltage profiles and (b) THD_v levels.



Figure 4. Simulation results obtained for Case 1c: (a) voltage profiles and (b) THD_V levels.

From Figure 3a it can be observed that there is an improvement in voltage profile for all load levels when capacitors are turned on. In addition to improving voltage profile, the shunt capacitors reduce the power losses of the system, as can be seen from Table 2. However, the sizes and connection points of the connected capacitors lead to high THD_V values in buses 6 to 21, as shown in Figure 3b. In relation to Case 1a, the maximum value of THD_V increases 46% and reaches 10.87%. This increase in voltage distortion levels is due to resonant conditions caused by shunt capacitors in combination with the load and feeder reactances.

Figure 4a,b respectively show voltage profiles and voltage distortions in the presence of shunt capacitors and DG (Case 1c). Comparing the results from these figures with those from Figure 3a,b, it is evident that in the presence of DG the voltage profile is significantly improved and the maximum total harmonic distortion is reduced. However, if the DG is turned on, harmonic distortions in some buses will still be higher than the allowed value of 5%. In addition, from Figure 4a it can be observed that RMS voltage at buses 12, 13,

and 14 for load level L= 1.0 is below the minimum acceptable level. The total system losses in this case are greatly reduced compared to that of Case 1b, and especially to Case 1a.

In order to demonstrate the accuracy of the DHPF method, which was used to estimate harmonic components, the power system harmonic analysis for Case 1a with a load level of 100% of the rated power



Figure 5. ETAP simulation results obtained for the IEEE 30-bus test system in Case 1a with load level of 100% of the rated power.



Figure 6. Simulation results obtained for Case 2a: (a) voltage profiles and (b) THD_V levels.



Figure 7. Simulation results obtained for Case 2b: (a) voltage profiles and (b) THD_V levels.



Figure 8. Simulation results obtained for Case 2c: (a) voltage profiles and (b) THD_V levels.

(L=1.0) was also performed using the Harmonic Analysis module of the Electrical Transient Analysis Program (ETAP) as shown in Figure 5. By comparing results from Figure 5 with corresponding results from Figure 2a,b, it can be observed that results obtained by the DHPF method are almost identical to those generated by the ETAP programme.

Case 2: The System Condition with Control Scheme

In order to demonstrate the effectiveness of the proposed algorithm, three different cases have been considered: (i) minimization of total active power losses (Case 2a), (ii) minimization of the voltage deviation (Case 2b), and (iii) a simultaneous minimization of the total active power losses and voltage deviation (Case 2c). Optimal dispatch scheduling results of the ULTC, capacitors, and DG for aforementioned objective functions obtained by the PPSOGSA are listed in Table 3–5, while the corresponding system parameters are shown in Table 6–8.

In Case 2a, the total active power losses for load levels of 0.5, 0.7, 1.0, 0.8, 0.9, and 0.6 are, respectively, 86.3439 kW, 117.5821 kW, 276.3345 kW, 155.9842 kW, 207.6395 kW, and 94.2094 kW – according to Table 6. By comparing these values with those obtained in Case 1a, it is clear that the minimization of losses as the objective in the Volt/VAr/THD_V control problem for each load level results in the reduction in losses of about 70%. Also, by adjusting the control variables to the

Load level	ТАР	Q _{C1} (kVAr)	Q _{C2} (kVAr)	Q _{C3} (kVAr)	Q _{C4} (kVAr)	Q _{C5} (kVAr)	Q _{C6} (kVAr)	Q _{C7} (kVAr)	<i>V_{DG}</i> (p.u.)
0.5	5	0	0	300	0	150	450	300	1.0495
0.7	7	500	500	300	0	150	750	600	1.0476
1.0	3	900	500	0	0	0	900	900	0.9984
0.8	0	900	600	150	0	150	900	600	0.9963
0.9	1	100	600	150	0	150	900	750	0.9941
0.6	6	900	600	300	0	300	600	450	1.0492

Table 3. Optimal settings of control variables in Case 2a.

Table 4. Optimal settings of control variables in Case 2b.

Load level	TAP	Q _{C1} (kVAr)	Q _{C2} (kVAr)	Q _{C3} (kVAr)	Q _{C4} (kVAr)	Q _{C5} (kVAr)	Q _{C6} (kVAr)	Q _{C7} (kVAr)	<i>V_{DG}</i> (p.u.)
0.5	1	0	0	600	0	300	900	900	1.0066
0.7	1	900	600	300	0	300	900	900	1.0093
1.0	2	900	600	150	0	0	900	900	1.0067
0.8	1	900	600	150	300	300	900	900	1.0113
0.9	1	900	600	150	0	300	900	900	1.0117
0.6	1	800	0	600	0	300	900	900	1.0075

Table 5. Optimal settings of control variables in Case 2c.

Load level	TAP	Q _{C1} (kVAr)	Q _{C2} (kVAr)	Q _{C3} (kVAr)	Q _{C4} (kVAr)	Q _{C5} (kVAr)	Q _{C6} (kVAr)	Q _{C7} (kVAr)	<i>V_{DG}</i> (p.u.)
0.5	0	900	600	300	0	0	900	900	1.0148
0.7	1	900	600	300	0	300	900	900	1.0095
1.0	3	900	600	150	0	0	900	900	1.0035
0.8	2	0	400	150	0	150	900	900	1.0077
0.9	2	900	600	150	0	300	900	900	1.0047
0.6	0	900	600	600	0	300	900	900	1.0113

Table 6. System parameters for Case 2a.

Load level	P ^L _{loss} (kW)	V ^L _{dev} (p.u.)	Min. <i>V_{RMS}</i> (p.u.)	Max. <i>V_{RMS}</i> (p.u.)	Max. <i>THD_V</i> (%)
0.5	86.3439	1.0434	1.0000	1.0500	3.9381
0.7	117.5821	1.1333	1.0000	1.0484	4.9998
1.0	276.3345	0.3477	0.9627	1.0191	4.9998
0.8	155.9842	0.3684	0.9688	1.0001	4.9994
0.9	207.6395	0.3972	0.9625	1.0063	5.0000
0.6	94.2094	1.0974	1.0000	1.0499	4.8952

Table 7. System parameters for Case 2b.

Load level	P ^L _{loss} (kW)	V ^L _{dev} (p.u.)	Min. <i>V_{RMS}</i> (p.u.)	Max. <i>V_{RMS}</i> (p.u.)	Max. <i>THD_V</i> (%)
0.5	167.9805	0.0950	0.9932	1.0070	4.4404
0.7	120.8037	0.1488	0.9865	1.0101	4.9998
1.0	323.1457	0.2542	0.9713	1.0128	5.0000
0.8	174.0573	0.1796	0.9841	1.0122	4.9991
0.9	275.8981	0.2111	0.9802	1.0127	4.9999
0.6	123.1326	0.1162	0.9906	1.0080	5.0000

optimal values from Table 3, an improvement in the voltage profile is achieved for all load levels, except for the first.

Based on the comparison of the values of the voltage deviation from Table 7, obtained for Case 2b, with the corresponding values obtained in Case 1a, it can be seen that the voltage deviation for each load is reduced by about 90%. In relation to Case 2a, the voltage deviation for load levels of 0.5, 0.7, 1.0, 0.8, 0.9, and 0.6 is reduced by, respectively, 90.89%, 86.87%, 26.89%, 51.25%, 46.85%, and 89.41%, while the power losses are increased by, respectively, 94.55%,

Load level	P ^L _{loss} (kW)	V ^L _{dev} (p.u.)	Min. <i>V_{RMS}</i> (p.u.)	Max. <i>V_{RMS}</i> (p.u.)	Max. <i>THD_V</i> (%)
0.5	100.3808	0.1395	0.9906	1.0152	3.7351
0.7	120.6617	0.1492	0.9868	1.0102	4.9994
1.0	282.0423	0.2912	0.9681	1.0192	5.0000
0.8	156.5806	0.1950	0.9805	1.0126	4.9995
0.9	212.4251	0.2297	0.9731	1.0128	4.9997
0.6	101.0992	0.1410	0.9870	1.0118	4.9549

Table 8.	System	parameters	for	Case	2c.
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2.74%, 16.94%, 11.59%, 32.87%, and 30.71%. From these results, it can be concluded that the minimization of the voltage deviation is in conflict with the minimization of losses. That means that satisfying one objective has opposite effect on the other objective.

By considering active power losses and voltage deviation as the objective function (Case 2c), the best compromise solutions obtained by the proposed PPSOGSA algorithm for the above-mentioned load levels are, respectively, 100.3808 kW, 120.6617 kW, 282.0423 kW, 156.5806 kW, 212.4251 kW, 101.0992 kW – for power losses, and 0.1395 p.u., 0.1492 p.u., 0.2912 p.u., 0.195 p.u., 0.2297 p.u., 0.141 p.u. – for the voltage deviation (see Table 8). In relation to Case 1a, the average reductions in losses and deviation are 67.89% and 87.47%, respectively. For this objective function, for all load levels it is assumed that the weighting factors w_P and w_V are, respectively, equal to 1 and 300.

The voltage profiles and THD_{V} values in the system for considered objective functions are shown in Figure 6–8.

From Figure 6–8 and Table 6–8, it can be observed that the maximum and minimum values of calculated bus voltage magnitudes (max. V_{RMS} and min. V_{RMS}), as well as the maximum values of THD_V for all load levels meet the limits defined in the IEEE Std. 519–1992 (1993). Reactive power output of the DG in the form of daily diagram is shown in Figure 9.



Figure 9. Reactive power output of the DG for three different optimization cases.

Method	Load level	Best solution	Worst solution	Average solution	Standard deviation	Load level	Best solution	Worst solution	Average solution	Standard deviation
PSO	1.0	290.1573	319.2000	302.2829	12.6273	0.9	214.3130	215.9083	215.0725	0.4550
PPSO		288.2476	301.6124	290.3730	4.2609		208.9545	212.0183	210.3400	0.9134
GSA		278.9881	285.5669	280.9947	1.8131		208.7613	211.9844	210.4755	0.9992
PSOGSA		277.9681	281.5000	279.2048	0.9445		207.3868	211.9655	209.2286	1.3884
PPSOGSA		276.3345	278.7754	277.5314	0.9188		207.6395	209.1188	208.3869	0.6408
PSO	0.8	160.5893	161.7462	161.0683	0.4032	0.7	121.2111	122.1773	121.6511	0.3014
PPSO		156.4828	157.3950	156.8178	0.3141		118.2552	119.1311	118.7628	0.2528
GSA		157.2565	158.6269	157.7987	0.3996		117.8737	119.7183	118.9977	0.6875
PSOGSA		156.2019	156.5913	156.3660	0.1105		117.5956	118.3521	117.8614	0.3283
PPSOGSA		155.9842	157.4168	156.5997	0.5453		117.5821	118.3483	117.8347	0.3088
PSO	0.6	96.6975	97.5197	97.0970	0.3000	0.5	87.8537	88.8952	88.3020	0.3696
PPSO		94.6254	95.7892	95.1976	0.4215		86.7876	88.0956	87.2867	0.4575
GSA		95.2363	96.4840	95.8093	0.4992		86.5669	88.5468	87.5941	0.6881
PSOGSA		94.2191	95.6465	94.4481	0.5108		86.4787	87.7518	86.7561	0.4895
PPSOGSA		94.2094	94.2147	94.2106	0.0022		86.3439	86.4851	86.3588	0.0444

Table 9. Performance of PSO, PPSO, GSA, PSOGSA, and PPSOGSA in Case 2a.

Table 10. Performance of PSO, PPSO, GSA, PSOGSA, and PPSOGSA in Case 2b.

Method	Load level	Best solution	Worst solution	Average solution	Standard deviation	Load level	Best solution	Worst solution	Average solution	Standard deviation
PSO	1.0	0.3585	0.6426	0.5081	0.0745	0.9	0.2467	0.2498	0.2482	0.0010
PPSO		0.3527	0.5169	0.3973	0.0614		0.2175	0.2294	0.2241	0.0037
GSA		0.2597	0.3582	0.2842	0.0382		0.2134	0.2520	0.2244	0.0110
PSOGSA		0.2542	0.3319	0.2646	0.0249		0.2111	0.2118	0.2114	0.0002
PPSOGSA		0.2542	0.2789	0.2569	0.0078		0.2111	0.2116	0.2113	0.0002
PSO	0.8	0.2139	0.2510	0.2226	0.0142	0.7	0.1852	0.1884	0.1863	0.0012
PPSO		0.1835	0.1886	0.1858	0.0017		0.1531	0.1558	0.1541	0.0009
GSA		0.1906	0.2266	0.2016	0.0126		0.1596	0.2328	0.1812	0.0277
PSOGSA		0.1809	0.1943	0.1850	0.0054		0.1488	0.1639	0.1540	0.0056
PPSOGSA		0.1796	0.1803	0.1798	0.0002		0.1488	0.1498	0.1491	0.0004
PSO	0.6	0.1580	0.1708	0.1615	0.0036	0.5	0.1321	0.1364	0.1338	0.0015
PPSO		0.1237	0.1285	0.1256	0.0013		0.0972	0.0998	0.0986	0.0008
GSA		0.1259	0.2086	0.1551	0.0301		0.1018	0.1821	0.1181	0.0238
PSOGSA		0.1162	0.1531	0.1251	0.0113		0.0950	0.0995	0.0961	0.0018
PPSOGSA		0.1162	0.1206	0.1184	0.0019		0.0950	0.0950	0.0950	0.0000

To verify the accuracy and efficiency of the proposed PPSOGSA algorithm, the results obtained for Case 2a and Case 2b are compared to those generated by using PSO, PPSO, GSA, and PSOGSA, as reported in Tables 9 and 10. Each algorithm was run 10 times for each of objective functions and each of load levels. Based on these results it is clear that the PPSOGSA has better performance in comparison to the other algorithms.

The convergence of the algorithms for the power losses minimization and voltage deviation minimization at the load level of 0.8 is shown in Figure 10a,b, respectively. It is clear that the proposed hybrid PPSOGSA algorithm can converge to its global optimal solutions in lower iterations compared to other algorithms used by the authors. From the aspect of running, the running time of the PPSOGSA-based approach is slightly longer for any optimization function and any load level. The average running time of one iteration for the hybrid PPSOGSA algorithm in the case of minimization of power losses was about 1 min.



Figure 10. Convergence profiles of different algorithms for the IEEE 30-bus test system in the case of (a) minimization of the power losses, and (b) minimization of the voltage deviation.

Conclusions

The main conclusions that can be drawn from the presented results and discussion of them are:

- The results obtained using the hybrid PPSOGSA algorithm are in accordance with the considered objective functions and all the specified constraints are met.
- It is shown that the performance of the system can be significantly improved in terms of reducing power losses and improving power quality by proper adjustment of control variables.
- By comparing the results obtained using the PPSOGSA algorithm with those obtained using the PSO, PPSO, GSA, and PSOGSA optimization algorithms, it is found that the PPSOGSA algorithm provides better solutions than the previous algorithms.
- The accuracy of the DHPF method which was used to estimate harmonic components is successfully verified using the ETAP programme.
- Regardless of the form of the objective function, the proposed PPSOGSA algorithm can be quickly and easily applied to any other system with linear and non-linear loads.

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